

|    | Type | L # | Hits  | Search Text  | DBs                               | Time Stamp       | Comments | Error Definition | Errors |
|----|------|-----|-------|--|-----------------------------------|------------------|----------|------------------|--------|
| 1  | BRS  | L1  | 32687 | tft OR tfts OR (thin adj film adj transistor\$1)   | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 15:55 |          |                  | 0      |
| 2  | BRS  | L2  | 309   | 1 and (common adj gate)  | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 15:17 |          |                  | 0      |
| 3  | BRS  | L3  | 309   | 1 and (common adj gate) and (common drain)   | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 15:17 |          |                  | 0      |
| 4  | BRS  | L4  | 33    | 1 and (common adj gate) and (common adj drain)   | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 15:23 |          |                  | 0      |
| 5  | BRS  | L6  | 171   | "5866445"<br>"5731613"<br>"5652452"<br>"5650636"<br>"5528056"<br>"5500538"<br>"5305128"<br>"5173792"<br>"4990981"<br>"4617563"<br>"4481511"<br>"3807831" | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 15:31 |          |                  | 0      |
| 6  | BRS  | L7  | 29    | 6 and (connected with parallel)  | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 16:11 |          |                  | 0      |
| 7  | BRS  | L10 | 344   | (tft OR tfts OR (thin adj film adj transistor\$1)) with connected with parallel  | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 16:04 |          |                  | 0      |
| 8  | BRS  | L11 | 16    | 10 and power and impedance   | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 16:12 |          |                  | 0      |
| 9  | BRS  | L13 | 42    | 12 and power and impedance   | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 16:15 |          |                  | 0      |
| 10 | BRS  | L16 | 4     | 15 and impedance   | USPAT; EPO; JPO; Derwent; IBM TDB | 2000/12/06 16:24 |          |                  | 0      |

|    | Type | L # | Hits | Search Text                   | DBs  | Time Stamp           | Comments | Error Definition | Errors |
|----|------|-----|------|-------------------------------|--|----------------------|----------|------------------|--------|
| 11 | BRS  | L17 | 207  | 12 and lateral                | USPAT;<br>EPO;<br>JPO;<br>Derwent;<br>IBM<br>TDB | 2000/12/<br>06 16:27 |          |                  | 0      |
| 12 | BRS  | L19 | 6    | 18 and 12                     | USPAT;<br>EPO;<br>JPO;<br>Derwent;<br>IBM<br>TDB | 2000/12/<br>06 16:34 |          |                  | 0      |
| 13 | BRS  | L20 | 37   | 12 and impedance and parallel | USPAT;<br>EPO;<br>JPO;<br>Derwent;<br>IBM<br>TDB | 2000/12/<br>06 16:57 |          |                  | 0      |